

**NOTICE**

Dt: 04-12-2017

**Schedule of I B.Tech. I & II SEM (R15) Supplementary DEC– 2017 END Lab Examinations**

**PROGRAMMING IN C AND DATA STRUCTURES LAB [5G123] I-II:**

Roll No.s	No. of Students	Date	Timings
15701A0106,15701A01B8 16701A0115,16701A0143 15701A02B7,16701A0285 15701A0348,15701A0393	08	08-12-2017	10.00 AM To 1.00 PM

**PROBLEM SOLVING THROUGH C LAB [5G113] I-I:**

Roll No.s	No. of Students	Date	Timings
16701A0258, 16701A0285	02	08-12-2017	02.00 PM To 5.00 PM

**ELECTRONIC DEVICES AND CIRCUITS LAB -I [5G312] I-I:**

Roll No.s	No. of Students	Date	Timings
16701A0258,16701A0285	02	09-12-2017	02.00 PM To 5.00 PM

**ELECTRONIC DEVICES AND CIRCUITS LAB-II [5G322] I-II:**

Roll No.s	No. of Students	Date	Timings
16701A0204,16701A0285 15701A0470,16701A0402 16701A0497,16701A04A0 16701A04C0	07	09-12-2017	02.00 PM To 5.00 PM

The students registered for the examinations are required to report to the Examiner concerned 15 minutes in advance of the commencement of the examination.



CHIEF CONTROLLER OF EXAMINATIONS